

CUBIC MEMORY ARRAY

ABSTRACT

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A cubic memory array is fabricated on a substrate having a planar surface. The cubic memory array includes a plurality of first select-lines organized in more than one plane parallel to the planar surface. A plurality of second select-lines is formed in pillars disposed orthogonal to the planar surface of the substrate. A plurality of memory cells are respectively coupled to the plurality of first and plurality of second select-lines.

10